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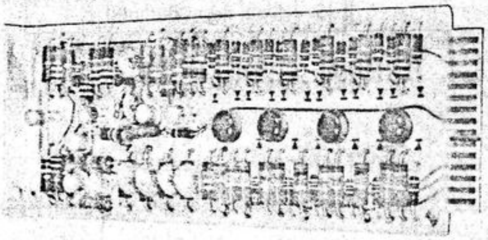
BIPCO® PRESET/RESET MODULE

TYPE
BIP-8613



The BIP-8613 preset/reset module works in conjunction with the 150 KC Uni-directional Counter, BIP-8055, and will preset and reset up to six counters. The module is of printed circuit construction, all solid state and can mount uniformly behind any panel.

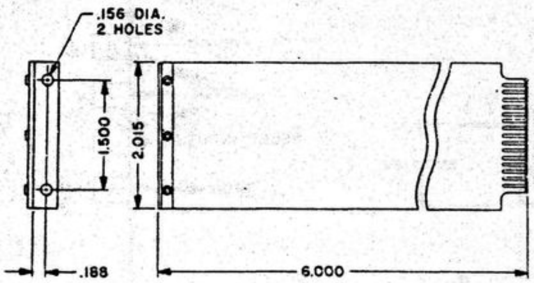


Figure 1. OUTLINE DRAWING

ELECTRICAL CHARACTERISTICS

INPUTS

RESET INPUTS (Figures 2 and 4, Notes 3 and 5)

Amplitude (negative)	6 V to 12 V
Pulse Duration	5 μsec min.
Leading Edge Rise Time	1 μsec. max.
Impedance (capacitance)	3,300 pf

PRESET INPUT (Note 1, Table 3)

OUTPUTS

PRESET OUTPUT (Note 4)

Amplitude (negative)	12 V
Pulse Duration	6 μsec. Typ.
Impedance	2.2 K ohm

RESET OUTPUT (Figure 4, Table 2)

POWER REQUIREMENTS

+12 Vdc ± 10% at 45 ma (Note 2)
−12 Vdc ± 10% at 2 ma

MECHANICAL SPECIFICATIONS

Outline Drawing	Figure 1
Terminal Connections	Table 1
Mounting Diagram	Figure 1
Receptacle (SR-115)	Figure 3 (Note 6)
Temperature — Operating	0°C to +85°C
— Non-Operating	−55°C to +100°C

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A	Not Used	1	First Preset (Note 1)
B	Not Used	2	Second Preset (Note 1)
C	−12 Vdc	3	Third Preset (Note 1)
D	Not Used	4	Fourth Preset (Note 1)
E	Not Used	5	Fifth Preset (Note 1)
F	Not Used	6	Sixth Preset (Note 1)
H	Not Used	7	Not normally used
J	Reset Input (Fig. 2)	8	+12 Vdc (Note 2)
K	Not Used	9	Not Used
L	Not Used	10	First Reset Output (Fig. 4)
M	Not Used	11	Second Reset Output (Fig. 4)
N	Preset Output (Note 4)	12	Third Reset Output (Fig. 4)
P	Flip-Flop Reset (Note 5)	13	Fourth Reset Output (Fig. 4)
R	Ground	14	Fifth Reset Output (Fig. 4)
S	Not Used	15	Sixth Reset Output (Fig. 4)

TABLE 1. TERMINAL CONNECTIONS

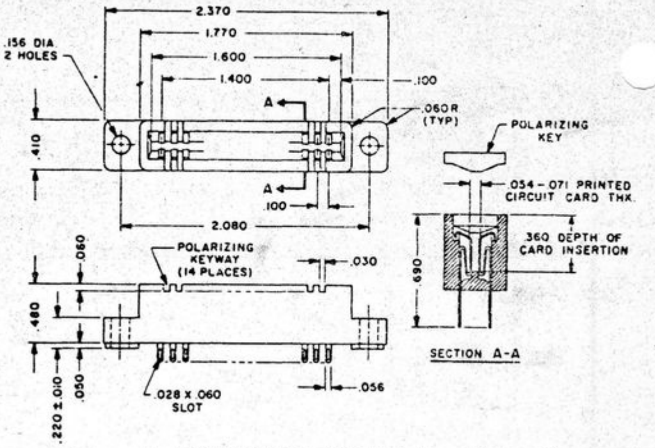


Figure 3. RECEPTACLE SR-115 (See Note 6)

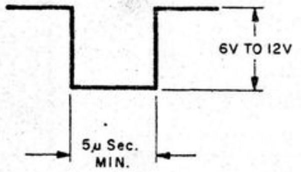
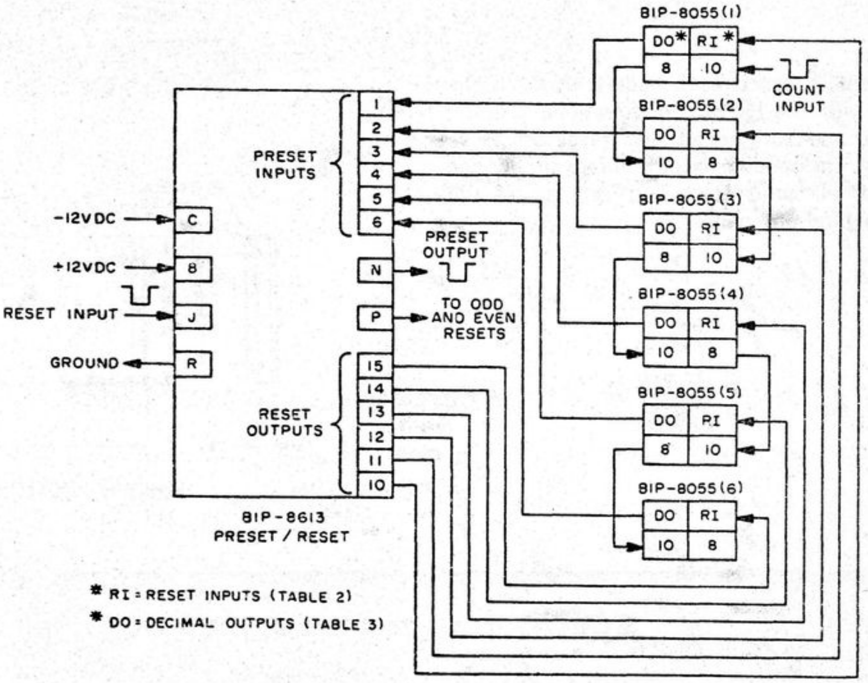


Figure 2. RESET PULSE



* RI = RESET INPUTS (TABLE 2)

* DO = DECIMAL OUTPUTS (TABLE 3)

RESET NUMBER	BIP-8055 TERMINAL
1	A
2	D
3	E
4	F
5	6
6	7
7	C
8	N
9	B
0	R
ODD	2
EVEN	5

TABLE 2

PRESET NUMBER	BIP-8055 TERMINAL
1	H
2	J
3	K
4	L
5	9
6	M
7	12
8	P
9	15
0	S

TABLE 3

Example:

1. Preset number "2347"
   Connect these BIP-8055 counter terminals, J (for 2), K (for 3), L (for 4) and 12 (for 7) to terminals 1, 2, 3 and 4, respectively and ground terminals 5 and 6 of the BIP-8613.
2. Reset number is "4825"
   Connect these BIP-8055 terminals, F (for 4), N (for 8), D (for 2) and 6 (for 5) to terminals 10, 11, 12 and 13, respectively of the BIP-8613. Also, for "odd and even," connect terminal P of the BIP-8613 to terminal 5 (for 4), 5 (for 8), 5 (for 2) and 2 (for 5) respectively of the four BIP-8055 counters.

Figure 4 — BIP-8613 BLOCK DIAGRAM AND INTERCONNECTION DIAGRAM (with BIP-8055)

NOTES

1. Preset inputs to terminals 1 through 6 are from the BIP-8055 output terminals. If not all six preset inputs are used, the unused terminals must be grounded (See Figure 4).
2. The +12 Vdc supply must be capable of supplying 650 ma max. for 5 µsec. during reset of six BIP-8055 counters.
3. After reset, a count pulse must not be applied to the BIP-8055 counter(s) for at least 3 µsec.
4. A 12 V, 6 µsec. negative pulse is available at terminal N, when the counter(s) have reached the preset number.
5. a. If an "odd" number is being reset for a particular counter; connect terminal 2 of the BIP-8055 to terminal P of the BIP-8613.
   b. If an "even" number is being reset for a particular counter: connect terminal 5 of the BIP-8055 to terminal P of the BIP-8613.
6. Receptacle SR-115 is supplied with the key unassembled; it must be inserted between terminals 9 and 10, K and L. Receptacle is not included as part of the module and must be ordered separately.

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